

Fig. 1

FIG. 2 is a block diagram of a Packet Forwarding Engine 106, according to one embodiment of the present invention. The Packet Forwarding Engine 106 is shown in communication with a Router Element 105 and a Packet Processing unit 207. The Packet Forwarding Engine 106 includes a Forwarding Table (FT) 116, a Packet Processing unit 207, and a Forwarding Plane (FPC) 205. The FPC 205 is connected to the FT 116 and the Packet Processing unit 207. The FPC 205 is also connected to a set of Packet Interface Cards (PICs) 201. The PICs 201 are connected to the FPC 205 and to the Router Element 105. The Router Element 105 is connected to the Packet Forwarding Engine 106 via a bidirectional arrow labeled "to RE 105". The Packet Forwarding Engine 106 is connected to the Packet Processing unit 207 via a bidirectional arrow labeled "207". The Packet Processing unit 207 is connected to the Router Element 105 via a bidirectional arrow labeled "Packets In/Out".

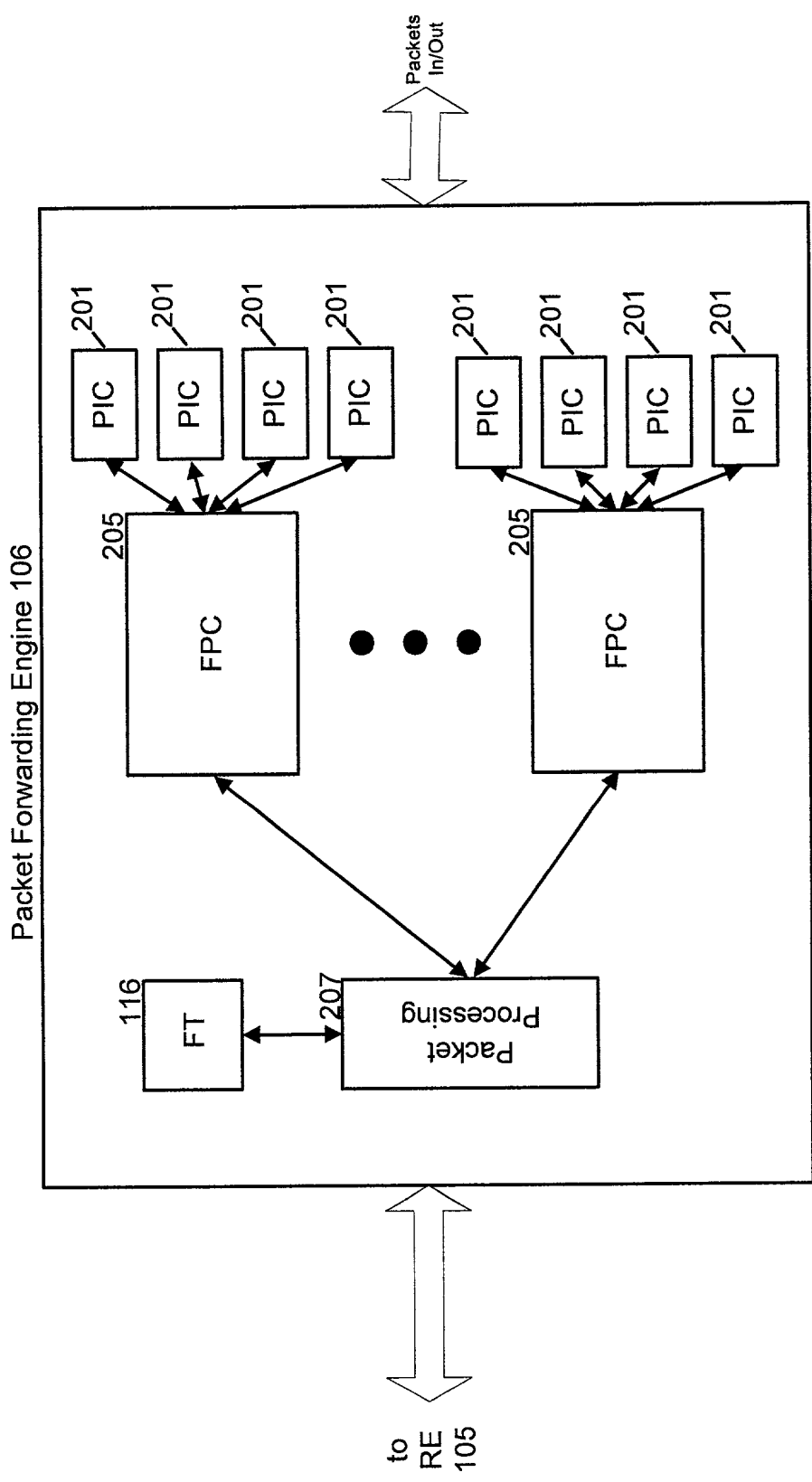


Fig. 2

FIG. 3 is a block diagram of a system 200 for controlling a power supply. The system 200 includes a power supply 201, a power monitor 339, an on/off control 338, a packet I/O manager 336, a memory 337, a physical interface 310, a serial bus controller 352, and a PIC 201. The power supply 201 provides power to the power monitor 339 and the on/off control 338. The power monitor 339 outputs a signal 327 to the on/off control 338. The on/off control 338 outputs a signal 326 to the packet I/O manager 336. The packet I/O manager 336 outputs a signal 331 to the physical interface 310. The physical interface 310 outputs a signal 310 to the PIC 201. The PIC 201 outputs a signal 321 to the power monitor 339. The power supply 201 also provides power to the power monitor 339 and the on/off control 338. The power monitor 339 outputs a signal 327 to the on/off control 338. The on/off control 338 outputs a signal 326 to the packet I/O manager 336. The packet I/O manager 336 outputs a signal 331 to the physical interface 310. The physical interface 310 outputs a signal 310 to the PIC 201. The PIC 201 outputs a signal 321 to the power monitor 339.

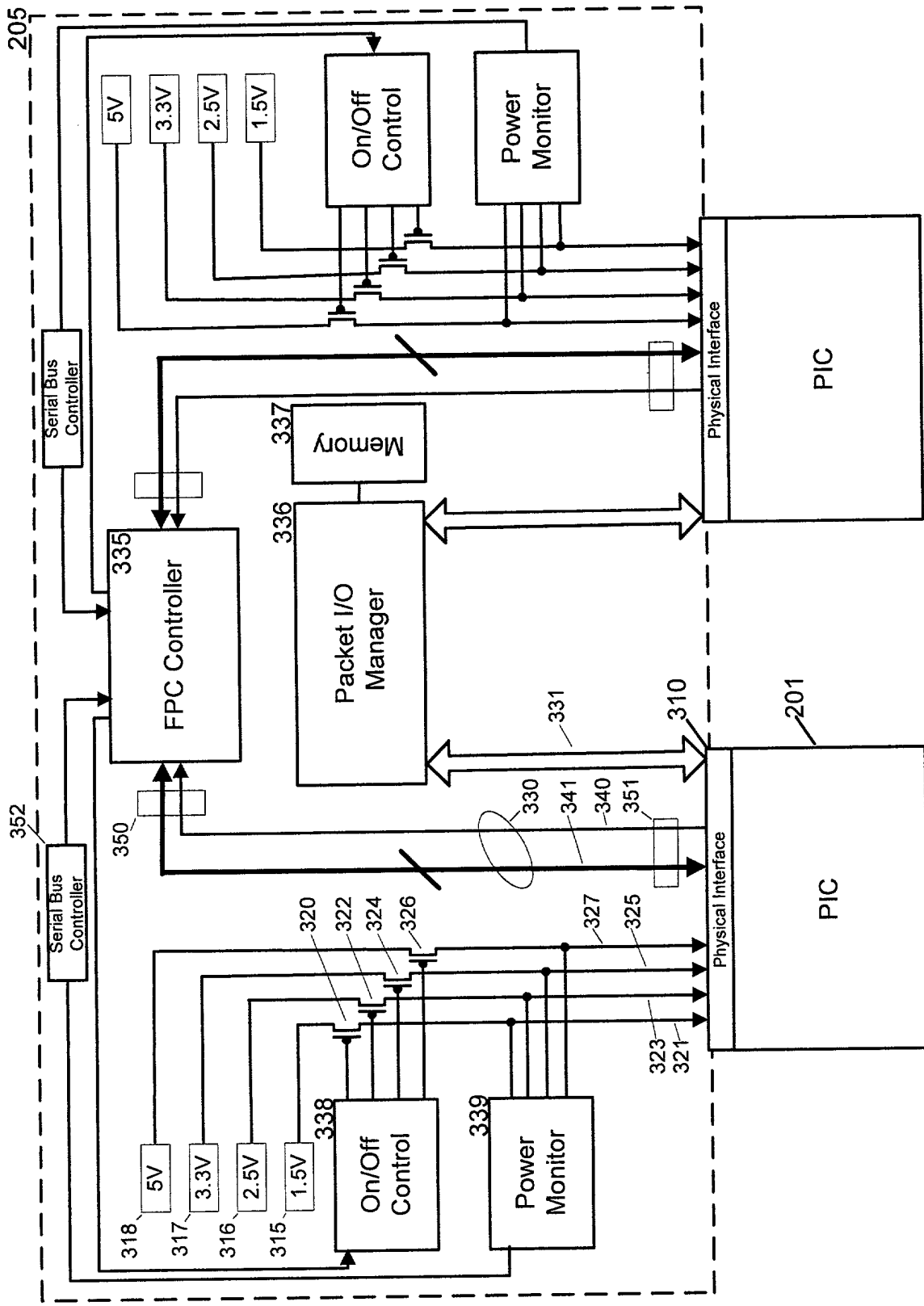


Fig. 3

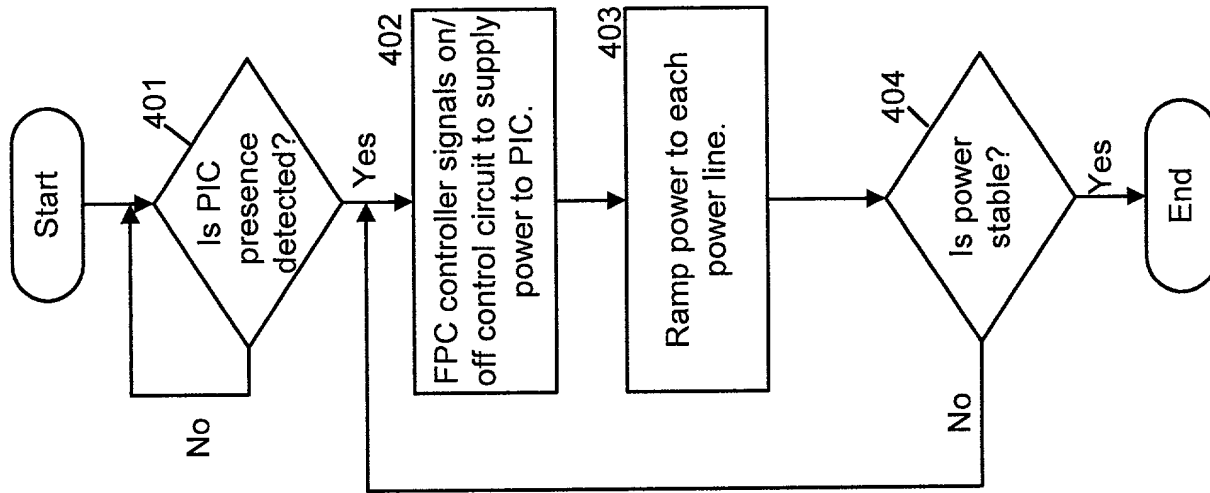


Fig. 4

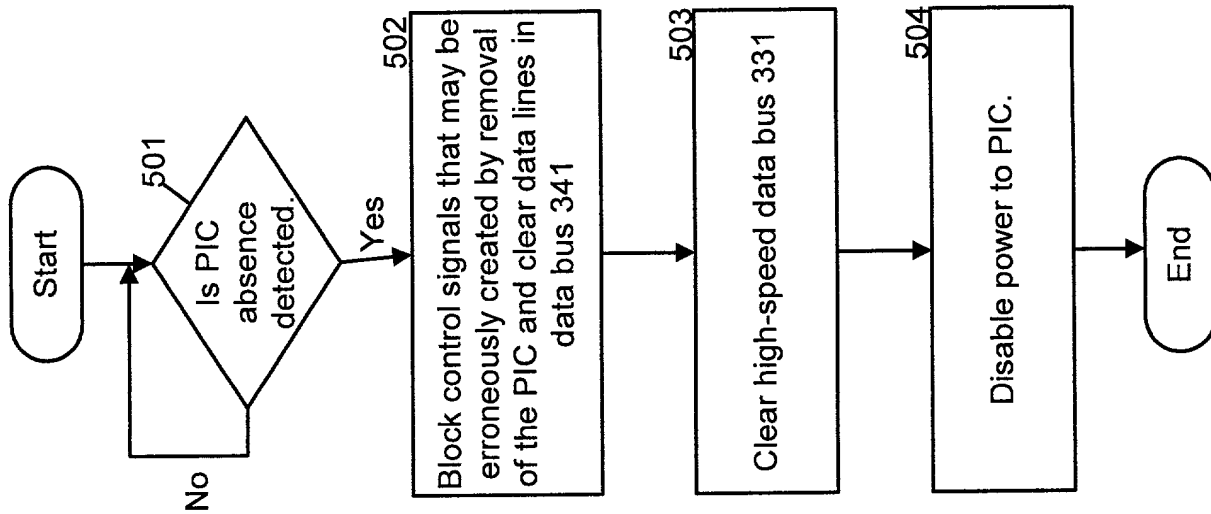


Fig. 5

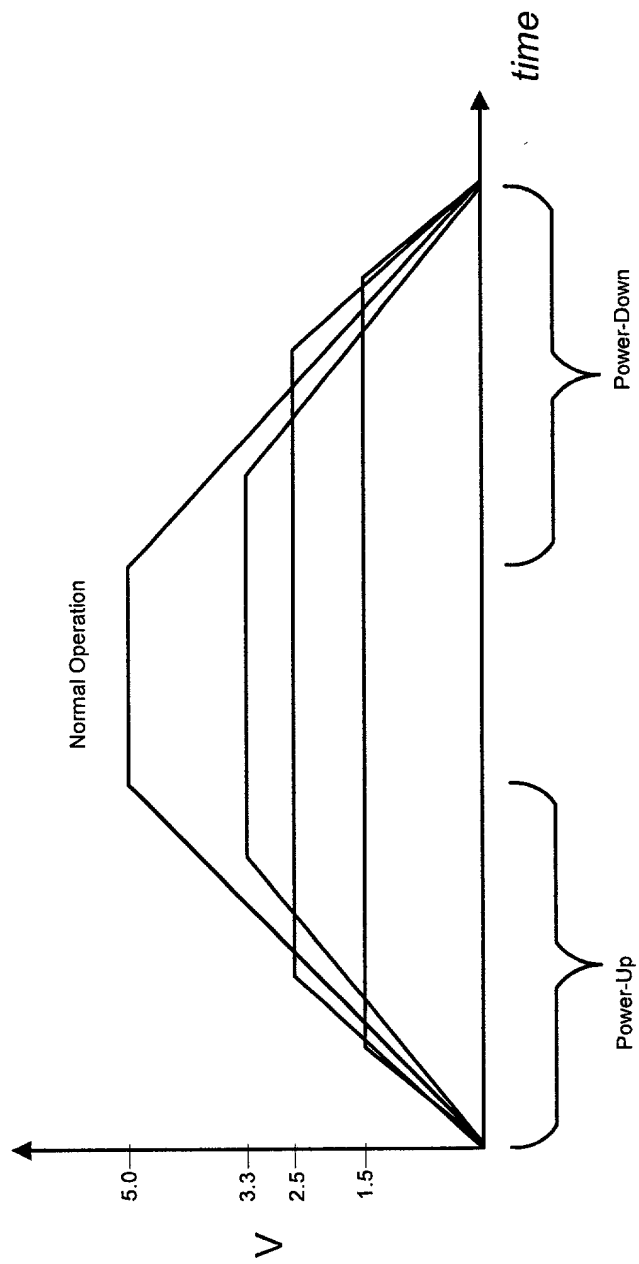


Fig. 6